

Fig.1

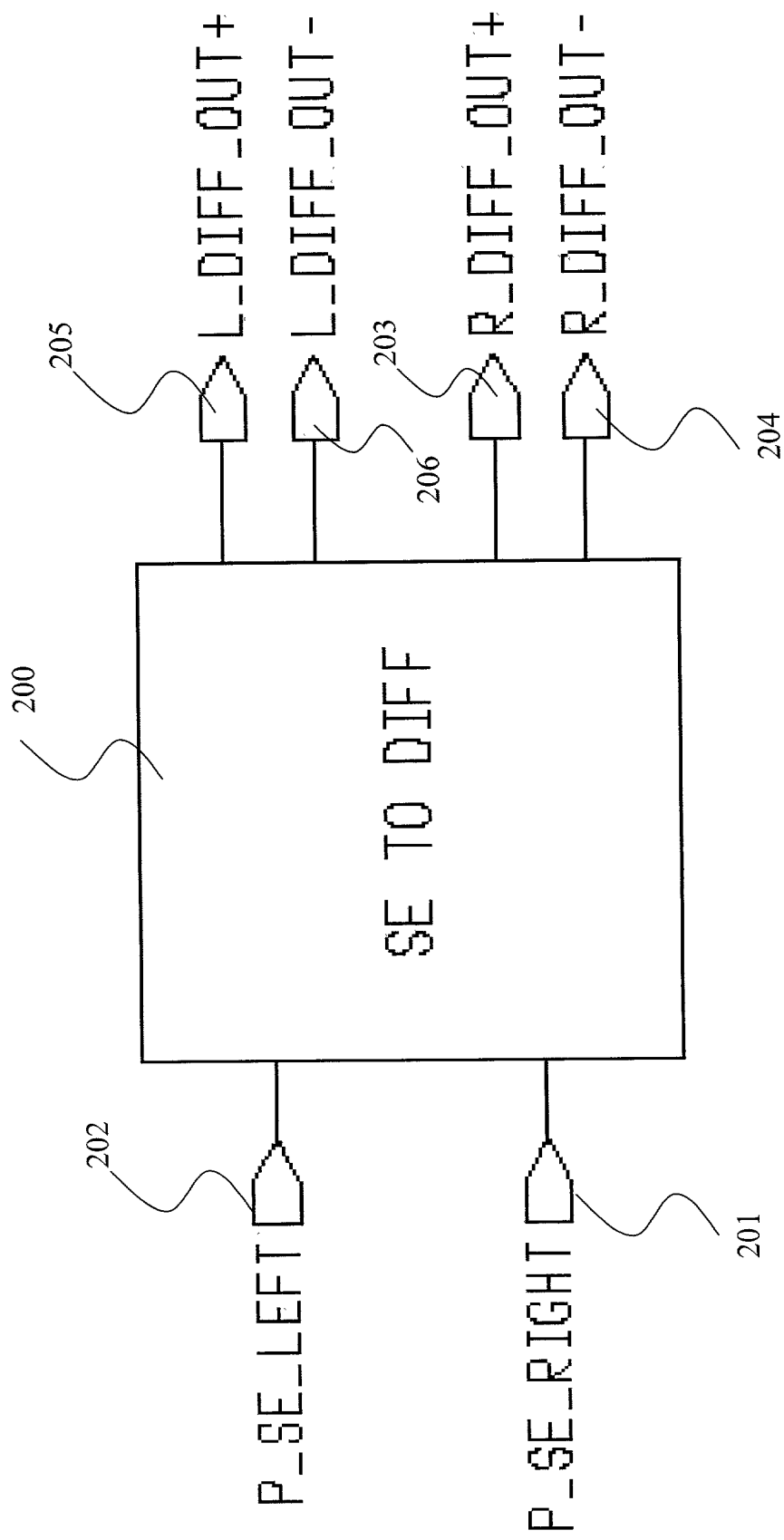


Fig. 2

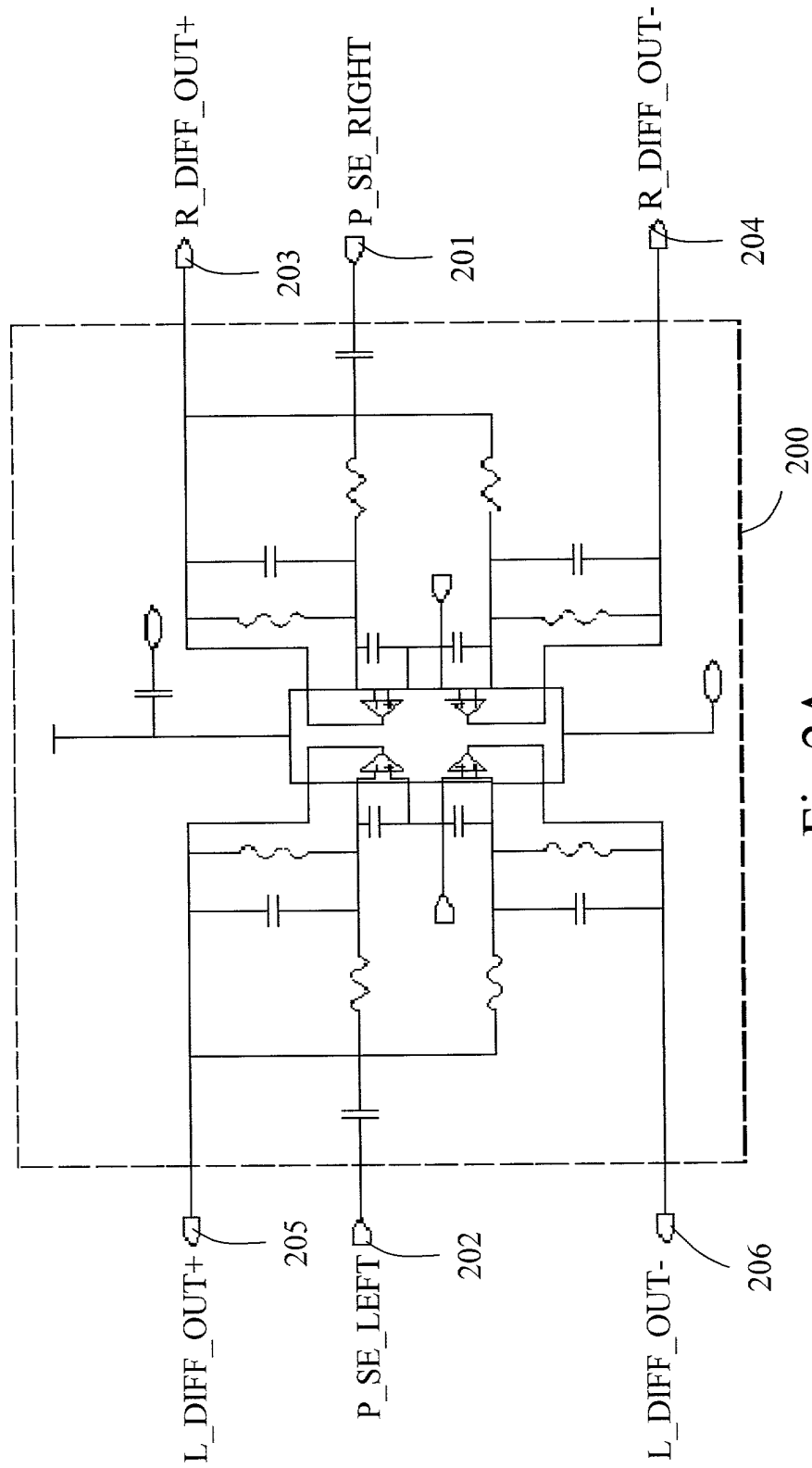


Fig.2A

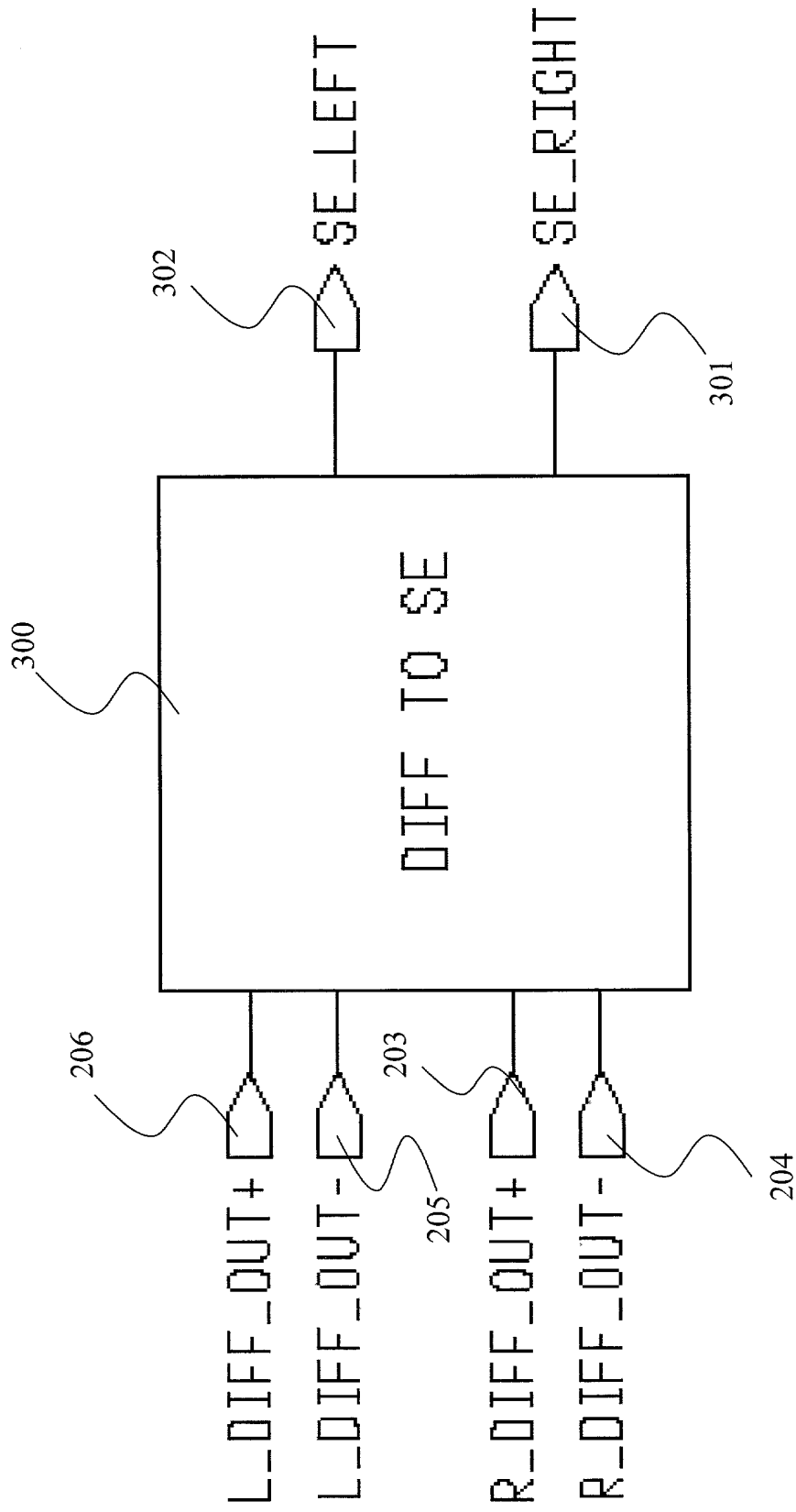


Fig.3

FIG. 3A is a schematic diagram of a differential signal processing circuit 300. The circuit 300 includes a first differential pair of transistors 301 and 302, a second differential pair of transistors 303 and 304, and a third differential pair of transistors 305 and 306. The circuit 300 is configured to process differential signals and generate differential outputs.

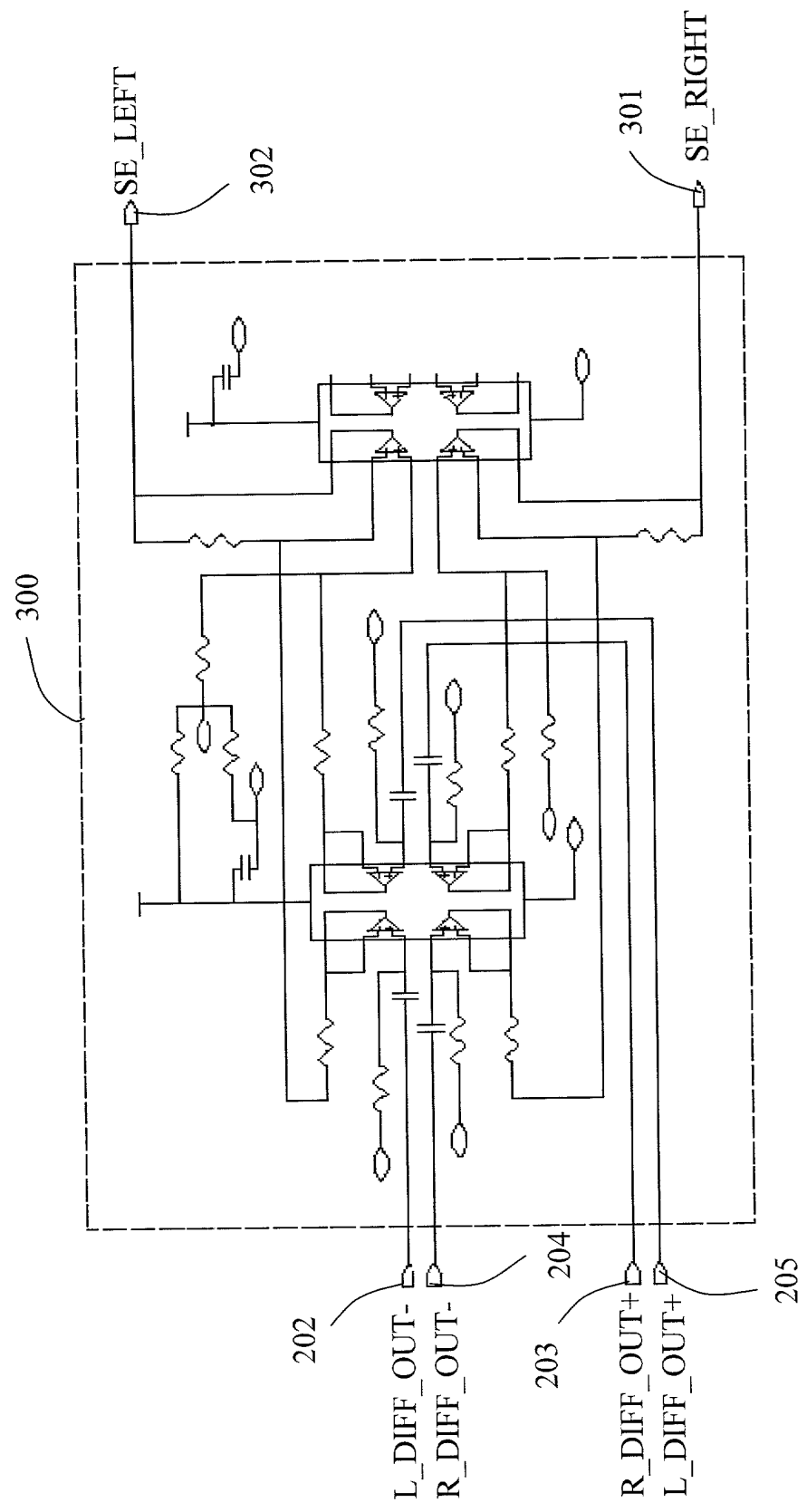


Fig.3A